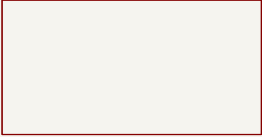


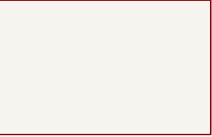
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A						
B						
C						
D						
	1	2	3	4	5	6

FPGA



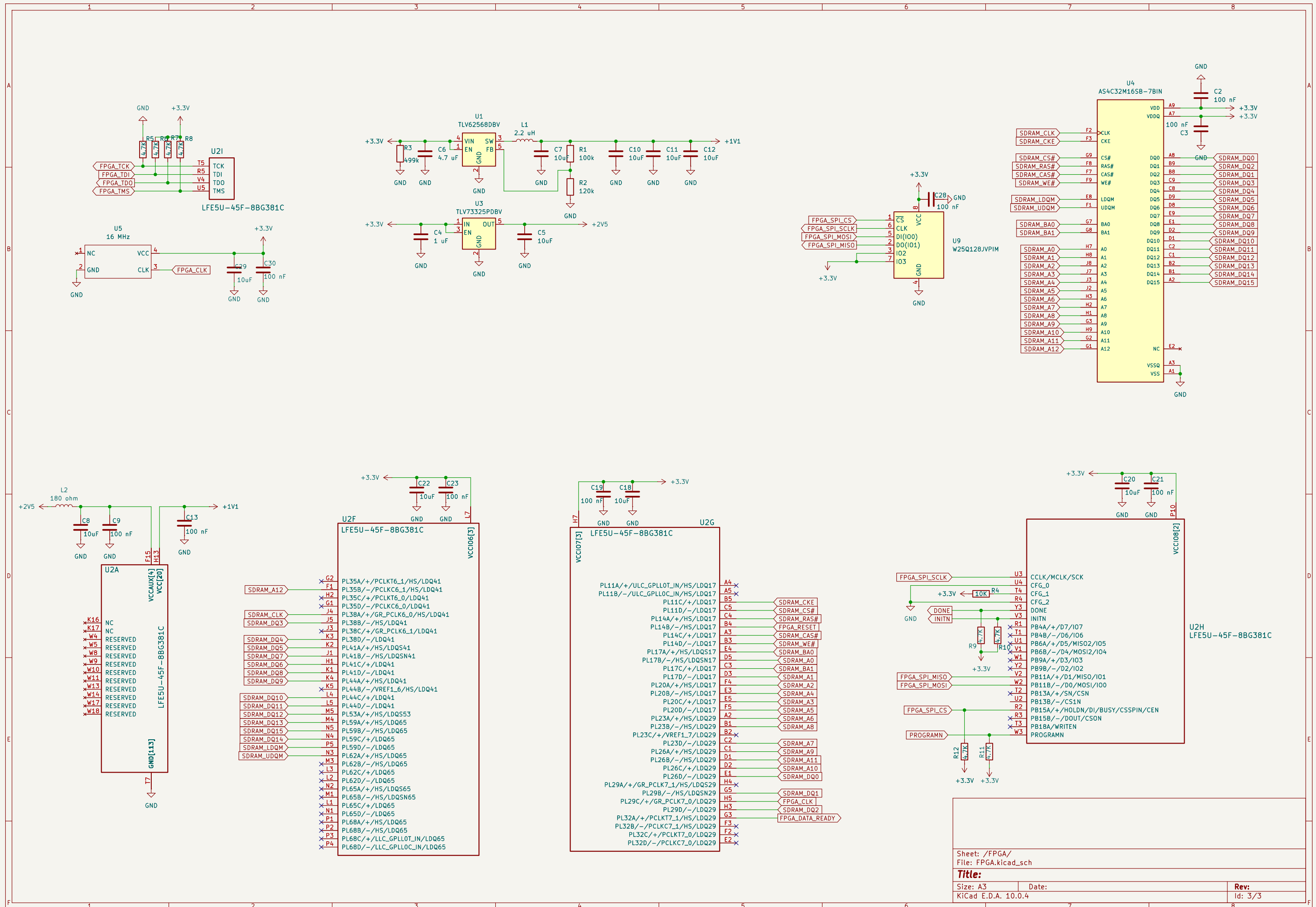
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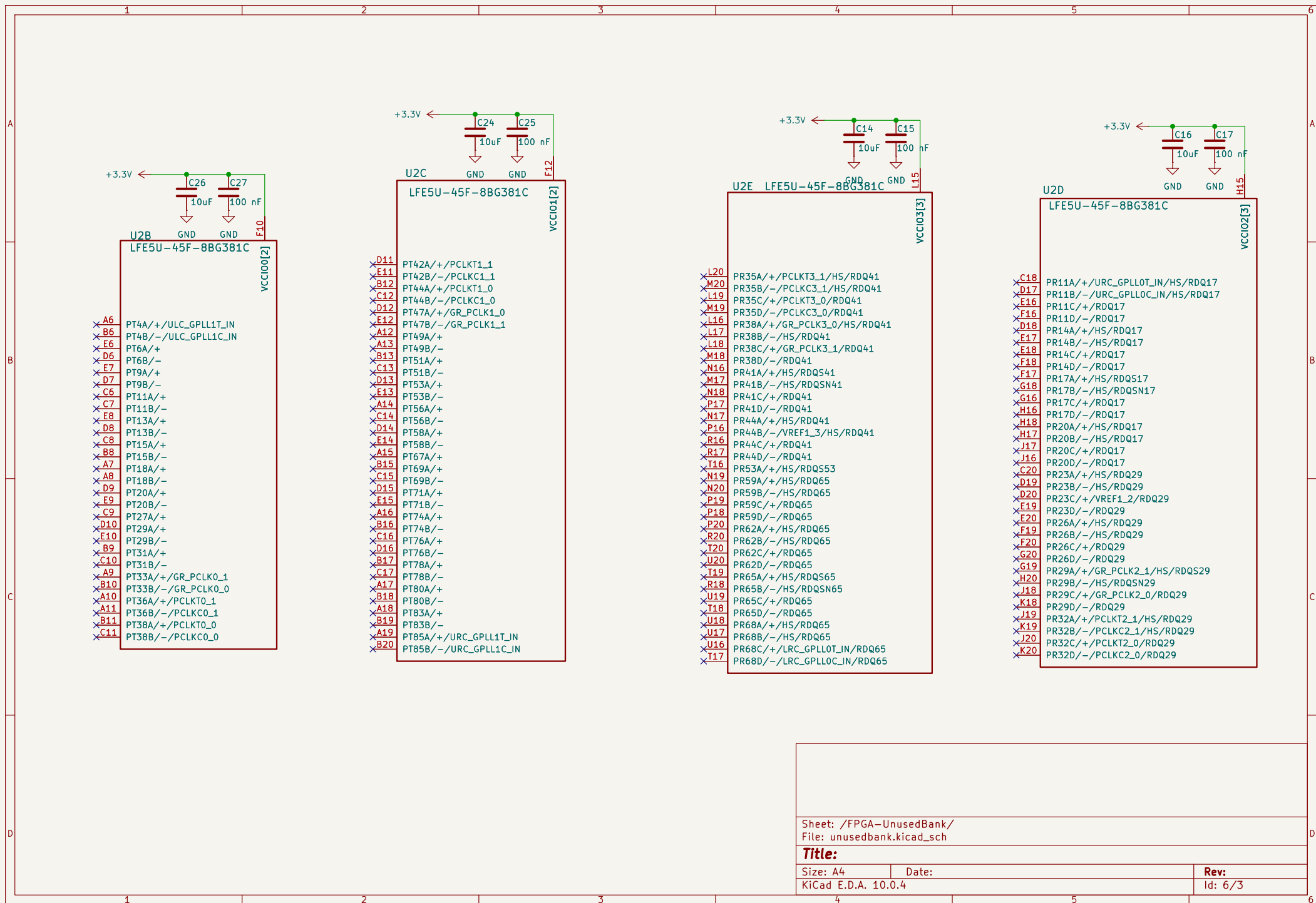
FPGA–UnusedBank



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