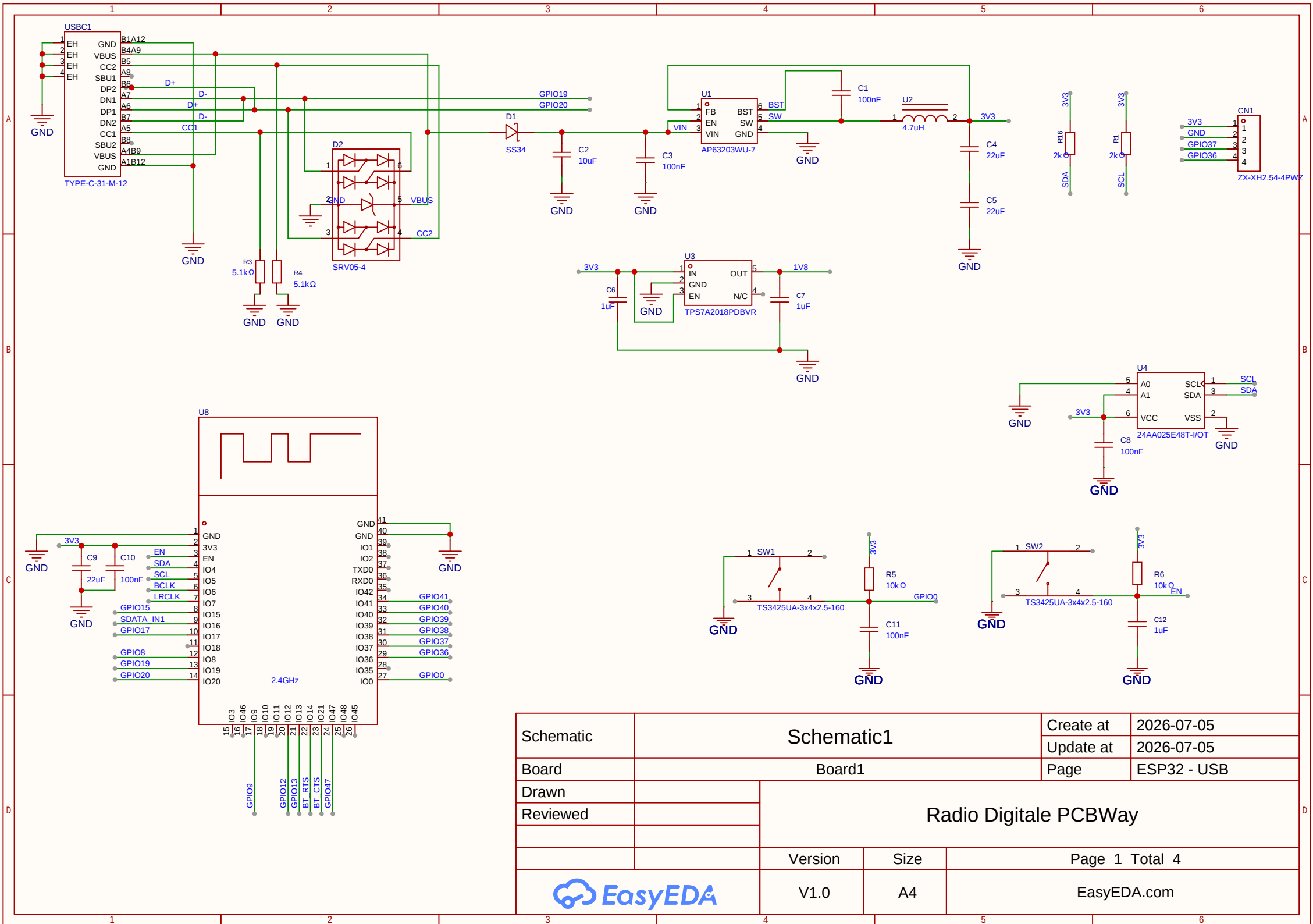
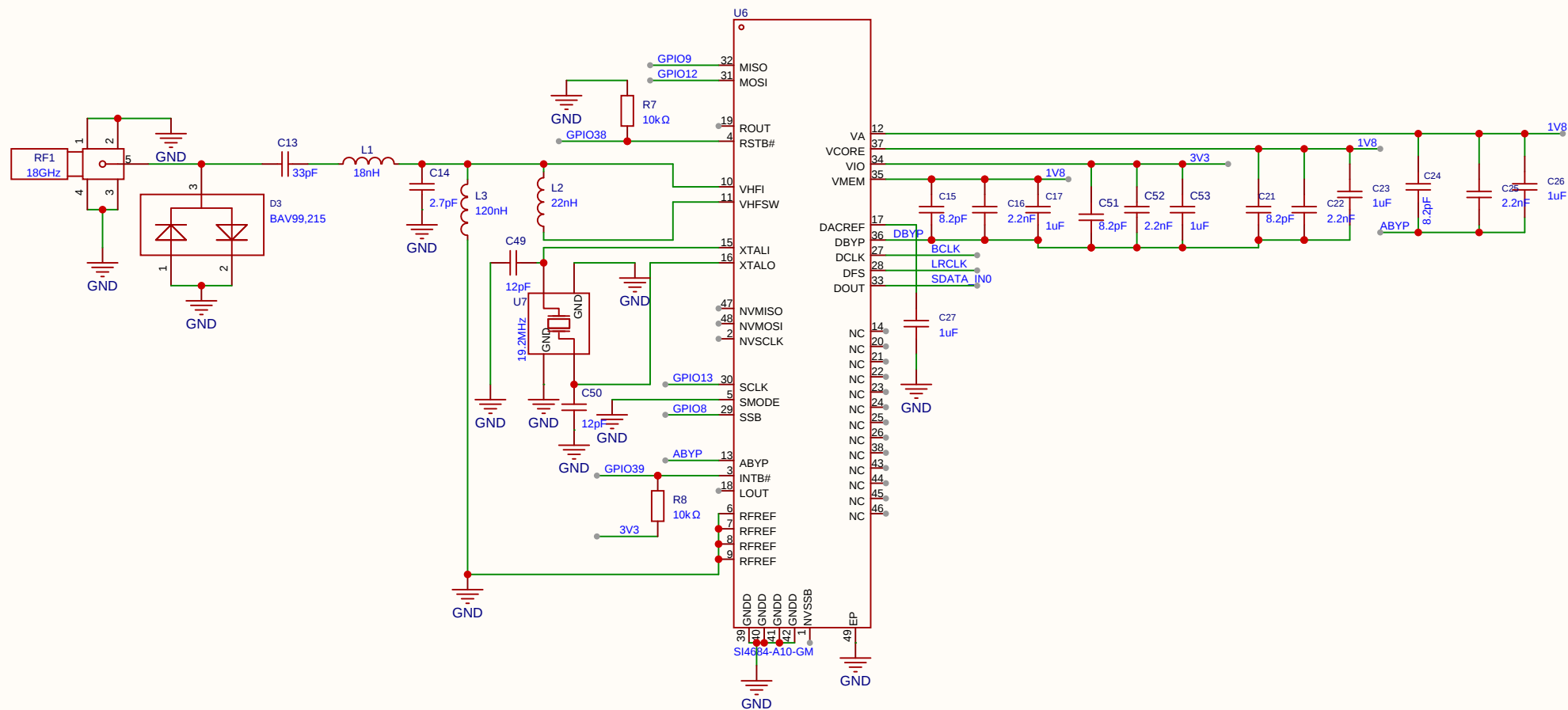


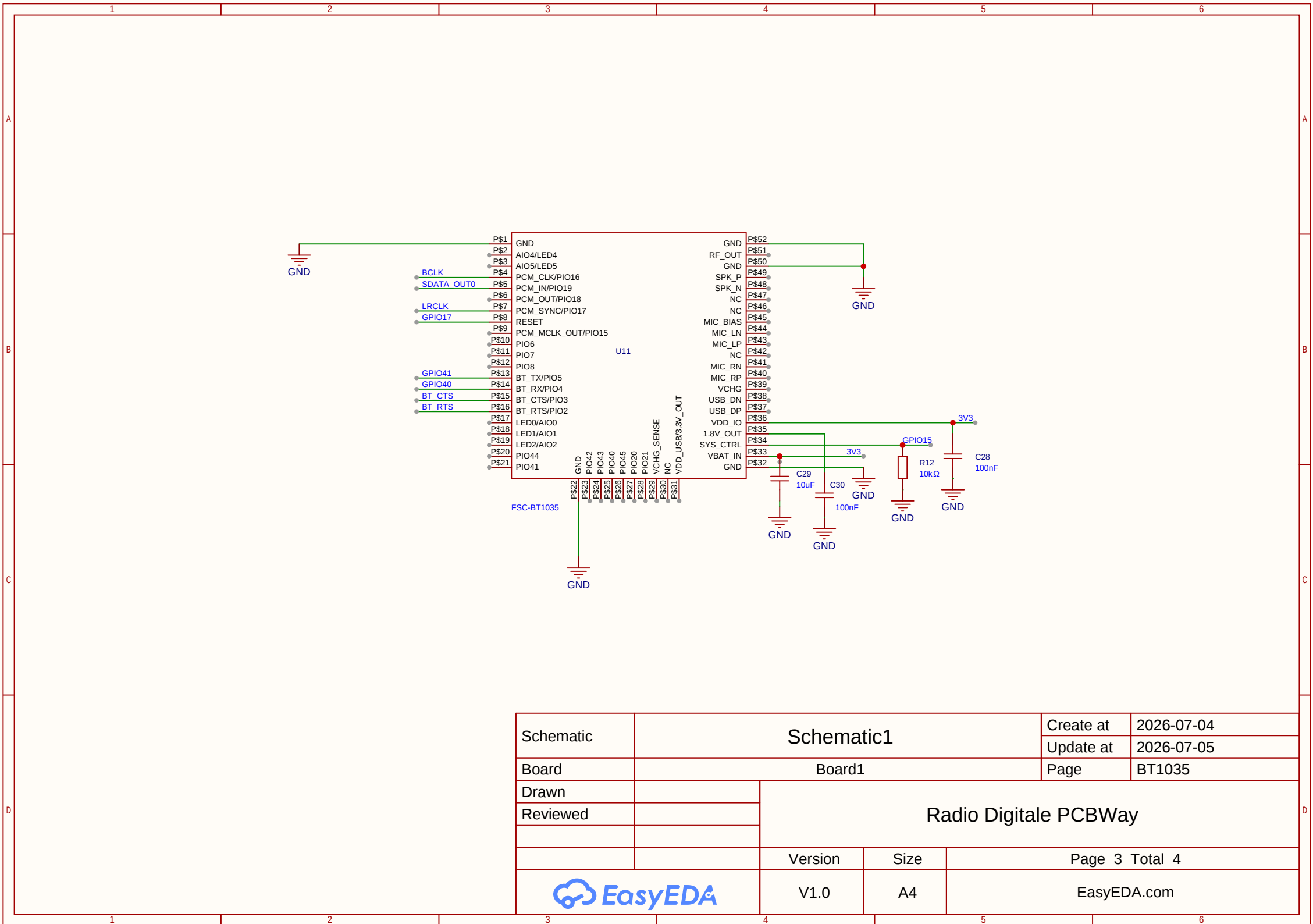


Schematic	Schematic1			Create at	2026-07-05
Board	Board1			Update at	2026-07-05
Drawn	Radio Digitale PCBWay			Page	ESP32 - USB
Reviewed					
		Version	Size	Page 1 Total 4	
		V1.0	A4	EasyEDA.com	

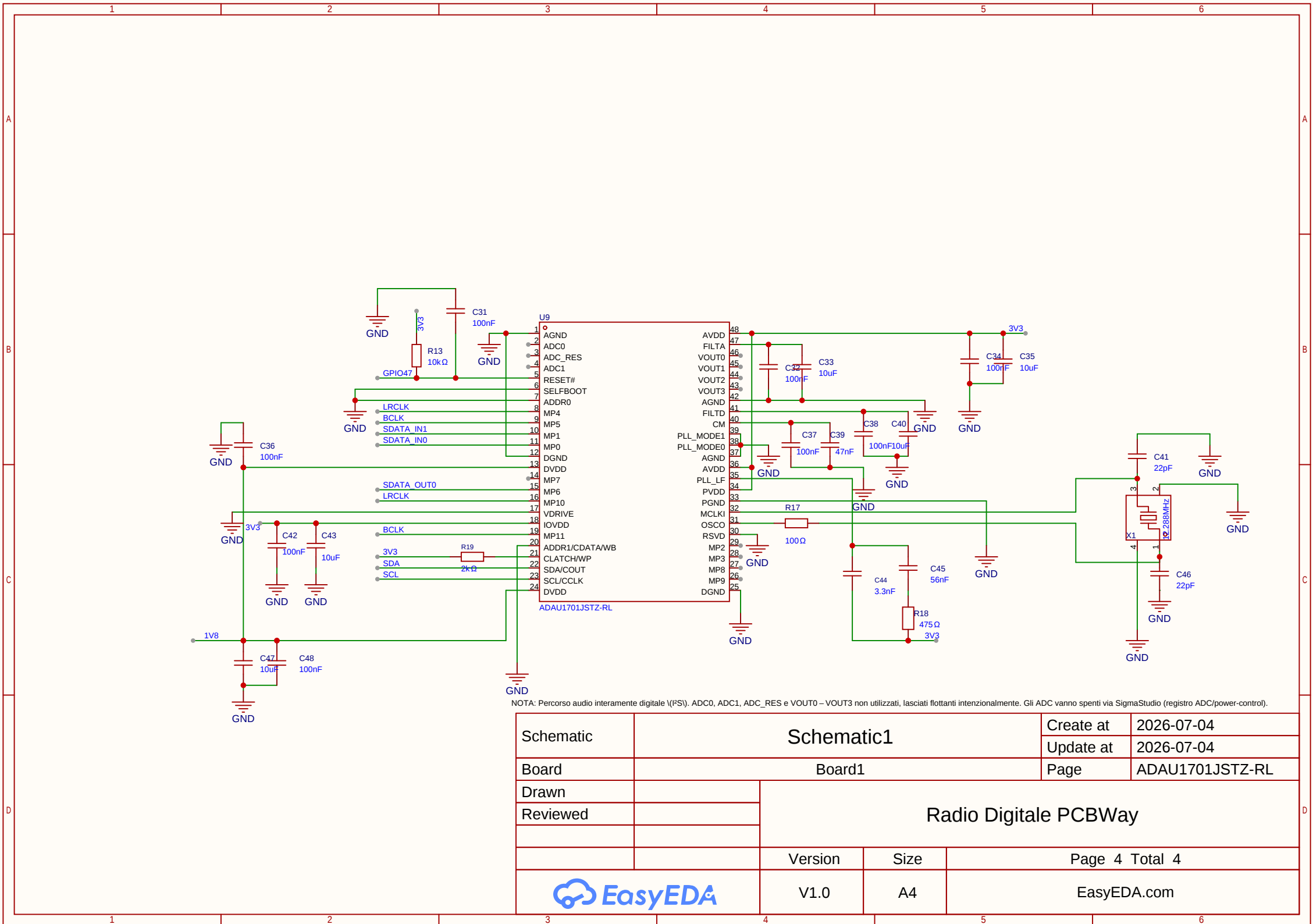




Schematic	Schematic1			Create at	2026-07-05
Board	Board1			Update at	2026-07-05
Drawn	Radio Digitale PCBWay				
Reviewed					
			Version	Size	Page 2 Total 4
			V1.0	A4	EasyEDA.com



Schematic	Schematic1			Create at	2026-07-04
				Update at	2026-07-05
Board	Board1			Page	BT1035
Drawn	Radio Digitale PCBWay				
Reviewed					
		Version	Size	Page 3 Total 4	
		V1.0	A4	EasyEDA.com	



NOTA: Percorso audio interamente digitale \(\P\S\). ADC0, ADC1, ADC_RES e VOUT0 - VOUT3 non utilizzati, lasciati flottanti intenzionalmente. Gli ADC vanno spenti via SigmaStudio (registro ADC/power-control).

Schematic	Schematic1			Create at	2026-07-04
				Update at	2026-07-04
Board	Board1			Page	ADAU1701JSTZ-RL
Drawn		Radio Digitale PCBWay			
Reviewed					
		Version	Size	Page 4 Total 4	
		V1.0	A4	EasyEDA.com	